AMENDMENTS TO THE CLAIMS

Please cancel claims 3 and 6 thru 8 without prejudice or disclaimer, amend claims 1, 2, 4, 5, 9 and 10, and add claims 11 thru 43, as follows:

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1 (Currently Thrice Amended). A flat panel display apparatus for receiving display information including video data and synchronizing data from a host [processing digital data in a serial digital communication, said display apparatus adapted for operation without need for any analog-to-digital converter (ADC) and phase-locked loop (PLL) circuit for signal conversion], said flat panel display apparatus comprising:

a receiver for reconstructing said display information;

a synchronizing signal generator connected to said receiver for generating a synchronizing signal by extracting the synchronizing data from said reconstructed display information;

a digital-to-analog converter (DAC) for converting said video data from said reconstructed display information to a corresponding analog video signal; and

an output terminal connected to said synchronizing signal generator and to said DAC for externally transferring said synchronizing signal and analog video signal to an analog display apparatus.

2 (Currently Thrice Amended). A flat panel display apparatus for receiving display information including video data and synchronizing data from a host [processing digital data in a serial digital communication, said display apparatus adapted for operation without need for any

analog-to-digital converter (ADC) or phase-locked loop (PLL) circuit for signal conversion], said flat panel display apparatus comprising:

a receiver for reconstructing said display information;

a synchronizing signal generator for generating a synchronizing signal by extracting the synchronizing data from said reconstructed display information;

a video data converter for converting said video data so as to correspond to a prescribed display mode;

a digital-to-analog converter (DAC) for converting said converted video data from said video data converter to a corresponding analog video signal; and

an output terminal for externally transferring said synchronizing signal and said analog video signal to an analog display apparatus[; and

a video data converter for converting line and dot numbers of said video data so as to correspond to a prescribed display mode when said synchronizing data has a different characteristic from said prescribed display mode, and said synchronizing signal generator generates said synchronizing signal corresponding to said display mode].

Claim 3. (Canceled)

4. (Currently Twice Amended) The display apparatus of claim [2] 1, further comprising:

a video data converter;

a liquid crystal display (LCD) driver for receiving data output from said video data

converter; and

[a] an LCD display panel for receiving an output from said LCD driver.

5 (Currently Thrice Amended). The display apparatus of claim 1, said analog display apparatus comprising:

an amplifier for receiving said video signal from said DAC via said output terminal, and for amplifying said video signal;

a deflection signal generator for receiving said synchronizing signal output from said synchronizing signal generator via said output terminal, and for generating deflection signals;

a high voltage generator for receiving an output from said deflection signal generator, and for generating a high voltage; and

a cathode ray tube (CRT) display for receiving said amplified video signal from said amplifier, [and output] said deflection signals from said deflection signal generator, and a high voltage from said high voltage generator.

Claims 6 thru 8. (Canceled)

9. (Currently Once Amended) In a flat panel display apparatus, comprising:

[a] receiver means for reconstructing video display information including video synchronization data from a host; and

[a] conversion means for converting [said] data to a corresponding video signal;

the improvement [*comprising*:] wherein said conversion [a] means [for converting] converts said data to [a] said corresponding video signal without utilization of an analog-to-digital converter (ADC) or a phase-locked loop (PLL) circuit.

10. (Currently Once Amended) In a method of processing display information containing video data and synchronizing data from a host processing digital data in a serial communication, said method comprising the steps of:

(1) reconstructing said display information to provide reconstructed display information;

(2) generating a synchronizing signal by extracting the synchronizing data from said reconstructed display information;

(3) converting said video data to a corresponding video signal; and

(4) transferring said synchronizing signal and said corresponding video signal to a display;

the improvement comprising[: a] the step [for] of converting said video data to a corresponding signal without utilizing an analog-to-digital converter (ADC) or a phase-locked loop (PLL) circuit.

11. (Newly Added) The display apparatus of claim 1, further comprising a video data converter connected between said receiver and said DAC for converting said video data so as to correspond to a prescribed display mode.

12. (Newly Added) The display apparatus of claim 11, wherein said video data converter converts said video data so as to correspond to the prescribed display mode when said synchronizing

signal has a characteristic different from the prescribed display mode.

13. (Newly Added) The display apparatus of claim 11, wherein said synchronizing signal generator generates said synchronizing signal in correspondence to the prescribed display mode.

14. (Newly Added) The display apparatus of claim 11, wherein said video data converter converts line and dot numbers of said video data so as to correspond to the prescribed display mode.

D4/ cont

15. (Newly Added) The display apparatus of claim 1, wherein said flat panel display apparatus operates without need for an analog-to-digital converter (ADC) or a phase-locked loop (PLL) circuit for signal conversion.

16. (Newly Added) A digital data processing system comprising the combination of a host and a flat panel display apparatus as recited in claim 1, said system further comprising a transmitter connected to said host for transferring said display information as serial data from said host to said receiver of said flat panel display apparatus.

17. (Newly Added) The system of claim 16, further comprising a video data converter connected between said receiver and said DAC for converting said video data so as to correspond to a prescribed display mode.

18. (Newly Added) The system of claim 17, further comprising:

a liquid crystal display (LCD) driver for receiving data output from said video data converter; and

an LCD display panel for receiving an output from said LCD driver.

19. (Newly Added) The system of claim 17, wherein said video data converter converts said video data so as to correspond to the prescribed display mode when said synchronizing signal has a characteristic different from the prescribed display mode.

D4 cont

20. (Newly Added) The system of claim 17, wherein said synchronizing signal generator generates said synchronizing signal in correspondence to the prescribed display mode.

21. (Newly Added) The system of claim 17, wherein said video data converter converts line and dot numbers of said video data so as to correspond to the prescribed display mode.

22. (Newly Added) The system of claim 16, wherein said flat panel display apparatus operates without need for an analog-to-digital converter (ADC) or a phase-locked loop (PLL) circuit for signal conversion.

23. (Newly Added) The display apparatus of claim 2, said analog display apparatus comprising:

an amplifier for receiving said video signal from said DAC via said output terminal, and for amplifying said video signal;

a deflection signal generator for receiving said synchronizing signal output from said synchronizing signal generator via said output terminal, and for generating deflection signals;

a high voltage generator for receiving an output from said deflection signal generator, and for generating a high voltage; and

a cathode ray tube (CRT) display for receiving said amplified video signal from said amplifier, said deflection signals from said deflection signal generator, and a high voltage from said high voltage generator.

D4 cont

24. (Newly Added) The display apparatus of claim 2, further comprising:

a liquid crystal display (LCD) driver for receiving data output from said video data converter; and

an LCD display panel for receiving an output from said LCD driver.

25. (Newly Added) The display apparatus of claim 2, wherein said video data converter converts said video data so as to correspond to the prescribed display mode when said synchronizing signal has a characteristic different from the prescribed display mode.

26. (Newly Added) The display apparatus of claim 2, wherein said synchronizing signal generator generates said synchronizing signal in correspondence to the prescribed display mode.

27. (Newly Added) A digital data processing system comprising the combination of a host

and a flat panel display apparatus as recited in claim 2, said system further comprising a transmitter connected to said host for transferring said display information as serial data from said host to said receiver of said flat panel display apparatus.

28. (Newly Added) The system of claim 27, further comprising:

a liquid crystal display (LCD) driver for receiving data output from said video data converter; and

an LCD display panel for receiving an output from said LCD driver.

D4 cont

29. (Newly Added) The system of claim 27, wherein said video data converter converts said video data so as to correspond to the prescribed display mode when said synchronizing signal has a characteristic different from the prescribed display mode.

30. (Newly Added) The system of claim 27, wherein said synchronizing signal generator generates said synchronizing signal in correspondence to the prescribed display mode.

31. (Newly Added) The system of claim 27, wherein said video data converter converts line and dot numbers of said video data so as to correspond to the prescribed display mode.

32. (Newly Added) The system of claim 27, wherein said flat panel display apparatus operates without need for an analog-to-digital converter (ADC) or a phase-locked loop (PLL) circuit for signal conversion.

33. (Newly Added) The display apparatus of claim 2, wherein said video data converter converts line and dot numbers of said video data so as to correspond to the prescribed display mode.

34. (Newly Added) The display apparatus of claim 2, wherein said flat panel display apparatus operates without need for an analog-to-digital converter (ADC) or a phase-locked loop (PLL) circuit for signal conversion.

DH cont

35. (Newly Added) In the flat panel display apparatus of claim 9, wherein said conversion means comprises a video data converter connected to said receiver means for converting said data so as to correspond to a prescribed display mode.

36. (Newly Added) In the flat panel display apparatus of claim 35, further comprising:

a liquid crystal display (LCD) driver for receiving data output from said video data converter; and

an LCD display panel for receiving an output from said LCD driver.

37. (Newly Added) In the flat panel display apparatus of claim 35, wherein said video data converter converts said data so as to correspond to the prescribed display mode when said synchronizing signal has a characteristic different from the prescribed display mode.

38. (Newly Added) In the flat panel display apparatus of claim 35, further comprising a

synchronizing signal generator which generates a synchronizing signal in correspondence to the prescribed display mode.

39. (Newly Added) In the flat panel display apparatus of claim 9, wherein said conversion means converts line and dot numbers of said data so as to correspond to the prescribed display mode.

40. (Newly Added) In the method of claim 10, wherein said converting step (3) comprises converting said video data so as to correspond to a prescribed display mode.

D4 cont'd

41. (Newly Added) In the method of claim 40, wherein said converting step (3) further comprises converting said video data so as to correspond to the prescribed display mode when said synchronizing signal has a characteristic different from the prescribed display mode.

42. (Newly Added) In the method of claim 40, wherein said generating step (2) comprises generating said synchronizing signal in correspondence to the prescribed display mode.

43. (Newly Added) In the method of claim 10, wherein said converting step (3) converts line and dot numbers of said video data so as to correspond to the prescribed display mode.